

ISLPED 2026

INTERNATIONAL SYMPOSIUM ON LOW POWER ELECTRONICS AND DESIGN

August 5-7, 2026

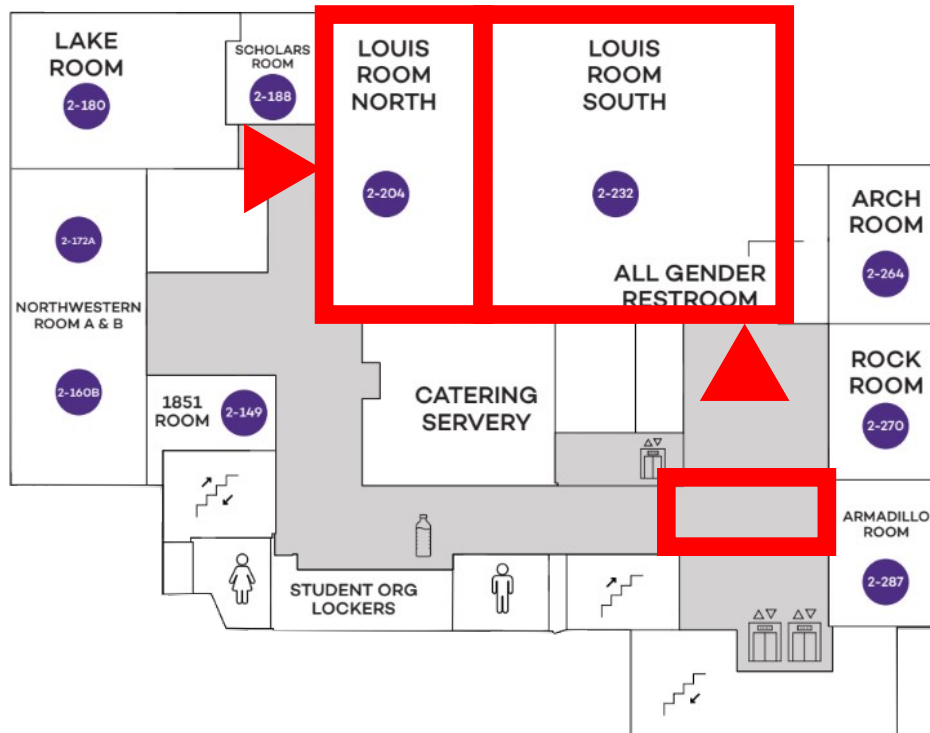
Northwestern University, Evanston, Illinois, USA

AMD 

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LOGISTICS



Louis Room South

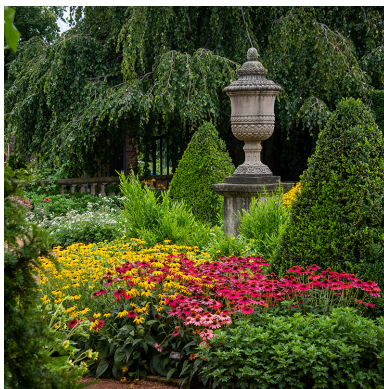
- Keynotes
- Regular paper sessions "A"
- Special session
- Women in Technology (WIT)
- Panel
- Lunch
- Poster and design contest demo

Louis Room North

- Regular paper sessions "B"

Lobby

- Registration
- Coffee breaks



Banquet @ Chicago Botanic Garden

Day 2 (Thursday) at 5:30pm

English Walled Garden in Chicago Botanic Garden (1000 Lake Cook Road, Glencoe, IL 60022)

Transportation provided

- Norris Center → Chicago Botanic Garden
- Chicago Botanic Garden → Hilton Orrington/Evanston

WEDNESDAY TECHNICAL PROGRAM

08:30–09:30	Registration + Breakfast
09:30–10:00	Welcome and Opening Remarks
10:00–11:00	Keynote 1
Louis South	Chairs: Aatmesh Shrivastava, Northeastern University & Younghyun Kim, Purdue University
	<i>In-Memory Computing for Next-Generation AI: Low-Power Design Applied to a New Era of Circuits and Architectures</i> Naveen Verma, Princeton University and EnCharge AI
11:00–11:20	Coffee Break
11:20–12:40	Session 1A: "ML-Driven Analysis and Optimization for Physical Design Signoff"
Louis South	Chairs: Sumit Kumar Mandal, IISc & Amit Agarwal, Intel
11:20–11:40	<i>GCU-Net: A Hybrid CNN-Transformer Architecture for Fast and Accurate Dynamic Voltage Drop Prediction</i> BEST PAPER CANDIDATE Freddy Gabbay and Mohammad Omri The Hebrew University, IL
11:40–12:00	<i>Timing- and Power-Aware Differentiable Repair of Minimum Implant Area Violations</i> Jinoh Cho, Minhyuk Kweon, Jinmo Ahn, Jeyeong Park and Seokhyeong Kang Pohang University of Science and Technology, KR
12:00–12:20	<i>WarPGNN: A Parametric Thermal Warpage Analysis Framework with Physics-aware Graph Neural Network</i> Haotian Lu, Jincong Lu, Sachin Sachdeva and Sheldon Tan University of California, Riverside, US
12:20–12:40	<i>BS-CSO: Backside-Assisted Clock-Signal Co-Optimization Framework</i> Ikkyum Kim, Geonhyeong Park and Heechun Park Ulsan National Institute of Science and Technology, KR
11:20–12:40	Session 1B: "Energy-Efficient Accelerators for Edge Intelligence"
Louis North	Chair: Xiaoxuan Yang, UVA
11:20–11:40	<i>TiMM-ViT: Token-Importance-Aware Token Merging with Multi-Exit Training and an FPGA-based Vision Transformer Accelerator for Image Classification</i> Soomin Rho, Sang-Ki Park, Subin Park, Kwangrae Kim, Min-Gwon Song and Ki-Seok Chung Hanyang University, KR
11:40–12:00	<i>FusionSense: Tri-Stage Near-Sensor Learning for Runtime-Adaptive Multimodal Edge Intelligence</i> Sanggeon Yun, Ryoza Masukawa, Minhyoung Na, Hyunwoo Oh, Yoshiki Yamaguchi, Wenjun Huang, SungHeon Jeong and Mohsen Imani University of California, Irvine, US · Kookmin University · Shibaura Institute of Technology, JP
12:00–12:20	<i>COSMOS: Designing Energy-Efficient Context-Aware Multimodal Cognitive Systems</i> Arghadip Das, Yatharth Agarwal, Soumendu Kumar Ghosh, Arnab Raha and Vijay Raghunathan Purdue University, US · Intel Corporation, US
12:20–12:40	<i>CMAx-CAMEL: A Coarse-to-Fine Adaptive, Memory-Efficient, and Low-Power Edge Processor for Contrast Maximization</i> BEST PAPER CANDIDATE Kyeongpil Min, Jongin Choi, Kyeongwon Lee and Woojoo Lee Chung-Ang University, KR
12:40–14:00	Lunch
14:00–15:00	Special Session: "Advancing Neuromorphic Testbeds: Integrating Memristor, Magnetic, and Superconducting Technologies for Next Generation Artificial Intelligence Computing Platforms"
Louis South	Chairs: Xiaoxuan Yang, UVA & Robinson Pino, DoE
14:00–14:15	<i>Advancing Neuromorphic Testbeds for Next-Generation AI Computing</i> Xiaoxuan Yang, University of Virginia
14:15–14:30	<i>Advancing Neuromorphic Hardware Testbeds: Biologically Plausible Neuron Circuits and Memristor In-Memory Computing</i> Yiran Chen, Duke University
14:30–14:45	<i>Exact Inference and Energy-Efficient Learning in Neuromorphic Testbeds with Magnetic Tunnel Junction Synapses</i> Jeonghwan Kim, University of Texas at Dallas
14:45–15:00	<i>Superconducting Optoelectronics for Neuromorphic Supercomputing</i> Jeffrey M. Shainline, Great Sky

15:00–16:00	Women in Technology (WIT): Panel Discussion
Louis South	Moderator: Kathy Hoover, AMD
	Powering the Future: A Conversation with Women Leaders in Electronics and Computing Mandy Pant, Intel Helen Li, Duke University
16:00–16:30	Women in Technology (WIT): Student Showcase Lightning Talks
Louis South	Chair: Kathy Hoover, AMD
	Synthesis and Optimization of Low-Power Low-Voltage CMOS Analog Integrated Circuits Amina Dada, University of Moulay Ismail Meknes, MA
	Exploiting Temporal Similarity for Energy-Efficient Inference on MCUs Soomin Lee, Purdue University, US
	Machine Learning Assisted Scan Chain Reordering for Low-Power VLSI Testing Sai Tejaswi P, CMR Institute of Technology, IN
	Reinforcement Learning for Vector Databases Sarah Glatter, University of Maine, US
	Tris-GCN: A 3D NAND Flash-based In-Storage Processing Architecture for Energy-Efficient GCN Acceleration Yi-Wa Wu, National Taiwan University, TW
	A Fused SRAM Compute-in-Memory Architecture for Spike-Based Sequential Learning Linda Mendez, Purdue University, US
16:30–17:00	Coffee Break + WIT Poster Session
Louis South	
17:00–18:20	Session 2A: "Data-Efficient and Precision-Optimized AI Accelerators"
Louis South	Chair: Minah Lee, UTD
17:00–17:20	L2Mersit: A Scaling-Free Sub-8-bit Data Format for On-Device Reliable Large Language Model Serving BEST PAPER CANDIDATE Myeongjin Kim, Hyeonseong Kim, Ik Joon Chang and Seungkyu Choi Kyung Hee University, KR · Yonsei University, KR
17:20–17:40	Exploiting Data Truncation for Lifetime Extension in ORB Accelerators Nicolás Ignacio Landeros, Alejandro Valero and Rubén Gran Tejero Universidad de Zaragoza, ES
17:40–18:00	CFoM-GS: An Anchor Gaussian Splatting Training Accelerator with Chunk-wise Footprint Matching Yiyang Sun, Han Liu, Renati Tuerhong and Tianyu Jia Peking University, CN
18:00–18:20	PVT-Insensitive Matrix-Vector Multiplier and ADC-less Tiling for Analog Computing and AI Accelerators Yongchao Liu, Lianlong Sun, Rong Zeng, Michael Huang and Hui Wu University of Rochester, US
17:00–18:20	Session 2B: "Robust and Efficient In-Memory Computing"
Louis North	Chair: Taewhan Kim, SNU
17:00–17:20	Dataflow-Preserving, Overhead-Free Weight Remapping for Fault-Tolerant ReRAM-Based In-Memory Computing Hyeonsu Bang and Jong Hwan Ko Sungkyunkwan University, KR
17:20–17:40	XL-HD: Extended Learning in Hyperdimensional Computing via Deterministic Projections for In-Memory Accelerators Sabrina Hassan Moon, Abu Kaisar Mohammad Masum, Sercan Aygun and Dayane Reis University of South Florida, US · University of Louisiana at Lafayette, US
17:40–18:00	A Top-Down Design Methodology for Accuracy-to-Noise Mapping in Analog Compute-in-Memory Systems: Evaluation on Mamba Isha Chakraborty, Laith A. Shamieh, Wei-Chun Wang, Han Cho and Saibal Mukhopadhyay Georgia Institute of Technology, US
18:00–18:20	MITRA: Reconfigurable, Low-Latency, and Power-Efficient In-Memory Stochastic Architecture for Transcendental Functions BEST PAPER CANDIDATE Farzad Razi, Mehran Moghadam, M. Hassan Najafi, Sercan Aygun and Marc Riede University of Minnesota · Case Western Reserve University · University of Louisiana at Lafayette

THURSDAY TECHNICAL PROGRAM

08:30–09:00	Registration + Breakfast
09:00–10:00	Keynote 2
Louis South	Chairs: Mehdi Kamal, USC & Srividhya Venkataraman, AMD
	Reconfigurable Nonlinear Computing in Silicon Wilfred G. van der Wiel, University of Twente and University of Münster
10:00–10:30	Coffee Break
10:30–11:50	Session 3A: "Emerging Circuits for Computing and Energy"
Louis South	Chair: Rajiv Joshi, IBM
10:30–10:50	A Current Mode Wireless Power Transfer with Deficit Energy Quantifier for Implanted Medical Devices BEST PAPER CANDIDATE Minsik Cho, Minhyeok Jeong, Hyunjun Choi, Mingyu Kim, Shin Han and Yoonmyung Lee Sungkyunkwan University
10:50–11:10	X-CODA: Xbar-Output-Dependent Compensation of In-Memory-Computing Errors for DNN Accuracy Enhancement Adrija Chakraborty, Akul Malhotra and Sumeet Kumar Gupta Purdue University, US
11:10–11:30	BB-CIM: A Back-Bias Tuned Analog Compute In-Memory in 22nm FD-SOI for Improved Power-efficiency Saideep Cherukuri, Apurba Prasad Padhy, Narasimha Vasishta Kidambi and Saibal Mukhopadhyay Georgia Institute of Technology, US
11:30–11:50	Tris-GCN: A 3D NAND Flash-based In-Storage Processing Architecture for GCN Acceleration Yi-Wa Wu, Jia-You Li, Chi-Jung Chen, Ching(Ryan) Cheng, Chia-Chun Wang, Chin-Fu Nien and Hsiang-Yun Cheng National Taiwan University, TW · National Yang Ming Chiao Tung University, TW · University of Illinois Urbana-Champaign, US · Purdue University, US · Academia Sinica, TW
10:30–11:50	Session 3B: "Efficient Computation for Edge and Brain-Inspired AI"
Louis North	Chairs: Itzik Adjiashvili, Huawei & Hiroshi Nakamura, Tokyo University
10:30–10:50	LayUp: Layer-wise Parallelization for Energy-Efficient Edge LLM Training Exploiting Unified Memory Characteristics Bang-San Lee and Young-Ho Gong Soongsil University, KR
10:50–11:10	Efficient On-Device Estimation of Transcutaneous Oxygen Using Machine Learning for Photoluminescence-Based Wearables Gokalp Cevik, Hakan Burak Karli, Vladimir Vakhter, Hohyeon Kim, Farnaz Niroui, Sercan Aygun, Bige Deniz Unluturk and Ulkuhan Guler Worcester Polytechnic Institute, US · Michigan State University, US · Massachusetts Institute of Technology, US · University of Louisiana at Lafayette, US
11:10–11:30	Vectoor-Space Projection and Unified Complex-Valued Acceleration for Scaling Matrix-Based Brain-Inspired Representations William Youngwoo Chung, Hyunwoo Oh, Calvin Yeung, Hansen Jin Lillemark, Hamza Errahmouni Barkam and Mohsen Imani University of California, Irvine · University of California, San Diego
11:30–11:50	PowerFlow-DNN: Compiler-Directed Fine-Grained Power Orchestration for End-to-End Edge AI Inference BEST PAPER CANDIDATE Paul (Yi-Chia) Chen, Jeongeun Kim, Wenbo Zhu, Yuanhan Li, Shunyao Huang, Chenjie Weng and Christopher Torng University of Southern California, US
11:50–12:50	Lunch
12:50–15:10	Posters
Louis South	
1	Unsupervised Low-Power ADC Calibration via Goertzel-Based Spectral Objective and Particle Swarm Search Nazareno Sacchi, Enrico Miotello, Filippo Borlenghi, Taekwang Jang and Amrith Sukumaran Swiss Center for Electronics and Microtechnology, CH · ETH Zurich, CH
2	EdgeVLA: A Heterogeneous Edge VLA Accelerator with Iteration-Aware Low-Rank Approximation for Embodied AI Renati Tuerhong, Yiyang Sun, Mingxuan Li and Tianyu Jia Peking University, CN
3	Application-Driven System Technology Co-Optimization for 2.5D Edge AI Platforms Anna Burdina, David Mallasen, alexandre Levisse, Davide Schiavone, Giovanni Ansaloni and David Atienza École Polytechnique Fédérale de Lausanne, CH
4	Post-Placement Optimization with Pin-Level Graph Isomorphism Network and Recurrent Q-Learning Kijung Kong and Heechun Park Ulsan National Institute of Science and Technology, KR

5	Congestion-Aware Integrated Optimization of Multi-Bit Flip-Flop Clustering and Placement Jiyun Ham and Heechun Park Ulsan National Institute of Science and Technology, KR
6	A Hardware-Based Multi-Stage Dynamic Power Management Architecture for Autonomous Low-Light Operation Charis Kouzinopoulos, Marcel Louis Meli, Martin Schellenberg, Philip J. Poole, Mathieu Bellanger, Matthias Kauer, Julien De Vos, Dimosthenis Ioannidis and Dimitrios Tzovaras Maastricht University, NL · Zurich University of Applied Sciences, CH · Microdul AG, CH · Lightricity Limited, GB · e-peas S.A., BE · Centre for Research and Technology Hellas, GR
7	FM-CAC: Carbon-Aware Control for Battery-Buffered Edge AI via Time-Series Foundation Models Kang Yang, Walid A. Hanafy, Prashant Shenoy and Mani Srivastava University of California, Los Angeles, US · University of Massachusetts, Amherst, US
8	GECKO: Graph-Evolving aware CheckPoinTer for Intermittent Systems Pin-Hong Li, Yan-Han Chang, Chun-Feng Wu and Pi-Cheng Hsiu National Yang Ming Chiao Tung University, TW · Academia Sinica, TW
9	QMC: Efficient SLM Edge Inference via Outlier-Aware Quantization and Emergent Memories Co-Design Nilesh Prasad Pandey, Jangseon Park, Onat Gungor, Flavio Ponzina and Tajana Rosing University of California, San Diego, US · San Diego State University, US
10	NEST: Adaptive Tree-Graph Vector Database for Energy-Constrained Edge Devices Sarah Lorell Glatter and Prabuddha Chakraborty University of Maine, US
11	Algorithm-Hardware Co-Design for Efficient Vector Symbolic Autonomous World Modeling Andrew Ding, William Youngwoo Chung, Nader Bagherzadeh and Mohsen Imani University of California, Irvine, US
12	Presto: Hardware Acceleration of Ciphers for Hybrid Homomorphic Encryption Yeonsoo Jeon, Lewis Liu, Mattan Erez and Michael Orshansky The University of Texas at Austin, US
13	Integrating Symbolic and Neural Mechanisms for Adversarially Robust Hyperdimensional Computing Hamza Errahmouni Barkam, Salaar Saraj, Zhen Ye, Xiangjian Liu, Haleh Alimohamadi, Nathaniel Bastian and Mohsen Imani University of California, Irvine, US · United States Military Academy, US
14	HyperSPACE: Sparse-Adder-Compatible Encoding for Efficient Hyperdimensional Computing on Digital CIM Arrays Yeong Hwan Oh, Do Yeong Kang, Juhong Park, Chanwook Hwang, Kang Eun Jeon and Jong Hwan Ko Sungkyunkwan University, KR · Korea Advanced Institute of Science and Technology, KR
15	MECA-CiM: A Shared-MicroExponent-aware Configurable Analog Compute-in-Memory Macro for Efficient Inference Wonkyung Han, Dohyun Kim, Jihoon Park, Juheun Lee, Sukhyun Choi, Wonjun Han and Jae-Joon Kim Seoul National University, KR
16	CiM-FLEX: An Integer-Only Flexible Periphery for Distributed Compute-In-Memory Architectures Irem Sanli, Michele Rossi, Elena Ferro, Abhishek Jha, Andreas Burg, Surinder Pal Singh, Thomas Boesch and Irem Boybat IBM Research, CH · STMicroelectronics, IT · STMicroelectronics, IN · École Polytechnique Fédérale de Lausanne, CH · STMicroelectronics, CH
17	Fusing the Attention Training Dataflow with Local Safe Softmax and Model-Independent Tiling JoonSeok Kim, Daeheon Lee, DongHwan Yoon, Jonghyeon Nam and Seokhyeong Kang Pohang University of Science and Technology, KR
18	MVP: A Mobile 3D-Stacked VLM Accelerator for Efficient Video Understanding by Leveraging Dynamic Sparse Attention Patterns Yifan Ding, Qianxu Wang, Dunshan Yu and Tianyu Jia Peking University, CN
19	H3-Attn: Attention-Efficient 3D DRAM PNM Processor with Hybrid Head Parallelism and Hierarchical SFU Yaolei Li, Wenbin Jia, Zhanchen Zhao and Yongpan Liu Tsinghua University, CN
12:50–15:10	Design Contest Demos
Louis South	Chairs: Ning Guo, AMD & Donghua Shin, Soongsil University
1	An Energy-Efficient Crossbar-Free Oscillatory Neural Network Accelerator for Near-Sensor Edge AI Jeongmin Jin, Mundo Jeong, Kyeongwon Lee, Chaebin Jung, Eunsu Cho, and Woojoo Lee Chung-Ang University, KR
2	NETRA – Energy-efficient Navigation at Edge towards Real-time Assistance for Visually Impaired Individuals Meenakshi Atkade and Sumit K. Mandal Indian Institute of Science, IN
3	Microwatt Microwave (M2) Oscillator: Enabling 105 μW, Stable and Standalone Transceivers Dhairya Shah, Pramuka Sooriya Patabandige, Rajashekar Reddy, Spanddhana Sara, Prabal Dutta, and Ambuj Varshney National University of Singapore, SG · University of California, Berkeley, US

4	EC-Sense: Ultra-Low-Power Wireless Soil Moisture Sensing using Energy Harvesting Yoganand Biradavolu and Bhuvana Krishnaswamy <i>University of Wisconsin, US</i>
5	Shared-Column Im2col for Energy-Efficient TinyML Convolution on Microcontrollers Seung-Ryeol Ohk, Won-Seok Chang and Young-Jin Kim <i>Ajou University, KR</i>
14:30–15:10	Coffee Break
15:10–16:30	Session 4A: "Specialized Accelerators for AI Workloads"
Louis South	Chair: Chun-Feng Wu, NYCU
15:10–15:30	PISA: Parameter-Efficient Image Super-Resolution Acceleration for Multiplierless Architecture Kihwan Kim, Xuan Truong Nguyen and Hyuk-Jae Lee <i>Seoul National University, KR</i>
15:30–15:50	Codebook-Based Acceleration for Ternary Language Model Inference Tomas Sousa Pereira, Ruibao Wang, Ikenna Nwozo, Haoran Geng and X. Sharon Hu <i>University Notre Dame, US · Jilin University, CN</i>
15:50–16:10	HotBa: A Heterogeneous Mamba Accelerator with Δ-Guided Early Rejection for Speculative Decoding Ghangmin Yun, Jueun Jung, Bokyoung Seo, Chaeyoon Kim, Junghyun Yoo and Kyuho Jason Lee <i>Yonsei University, KR · Ulsan National Institute of Science and Technology, KR</i>
16:10–16:30	SA-Kura: An Energy-Efficient Systolic Array Accelerator for Locally-Coupled Kuramoto Drift in Diffusion Sampling BEST PAPER CANDIDATE Jeongmin Jin, Kyeongwon Lee, Mundo Jeong, Jongin Choi and Woojoo Lee <i>Chung-Ang University, KR</i>
15:10–16:30	Session 4B: "Secure and Efficient Computing Architectures"
Louis North	Chair: Sumeet Gupta, Purdue University
15:10–15:30	Area-Efficient FHE Acceleration via Ciphertext-Level Parallelism and Shared-Key Reuse BEST PAPER CANDIDATE Youngtae Ahn, Jaehyung Kim and Hyungmin Cho <i>Sungkyunkwan University, KR</i>
15:30–15:50	Mitigating Clock Glitch Attack via Electromagnetically Engineered Interconnects Dewan Saiham, Md. Omar Faruk Noman, Sazadur Rahman and Soumitra Joy <i>University of Central Florida, US · University of North Carolina at Charlotte, US</i>
15:50–16:10	Beyond Static Policies: Dynamic-PRAC for Balanced and Efficient Rowhammer Mitigation Jeonghyun LEE, Minwoo Ahn, Jisung Park and Jinkyu Jeong <i>Yonsei University, KR · Pohang University of Science and Technology, KR</i>
16:10–16:30	ViViD-GS: Voxel-Constrained Representation and Voxel-Centric Accelerator for Real-Time 3D Gaussian Splatting Seung-Eon Hwang and Jongsun Park <i>Korea University, KR</i>
17:30–20:30	Banquet
	Chicago Botanic Garden (English Walled Garden) 1000 Lake Cook Rd, Glencoe, IL 60022

FRIDAY TECHNICAL PROGRAM

08:30–09:00	Registration + Breakfast
09:00–10:00	Keynote 3
Louis South	Chairs: Younghyun Kim, Purdue University & Srividhya Venkataraman, AMD
	<i>Near-Data Computing: The Energy Imperative</i> Hoshik Kim, SK Hynix
10:00–11:00	Panel Discussion
Louis South	Moderator: Srividhya Venkataraman, AMD
	<i>Accelerating Silicon Innovation with GenAI</i> Massoud Pedram, University of Southern California Kaushik Sengupta, Princeton University Hamid Shojaei, Cadence
11:00–11:20	Coffee Break
11:20–12:40	Session 5A: "Efficient Sensing and Neuromorphic Accelerators"
Louis South	Chair: Ulkuhan Guler, WPI
11:20–11:40	<i>A Neural-Feedback-Driven Event Camera for Robust and Efficient Vision Processing</i> Jaehyeon So, Houk Lee, Chanwook Hwang, Woosung Chung, Jaehyuk Choi and Jong Hwan Ko <i>Sungkyunkwan University, KR</i>
11:40–12:00	<i>Space-CIM: Enabling Compute-In-Memory Accelerators for Thermally-Constrained Space Platforms</i> Sohan Salahuddin Mugdho, Md Shahedul Hasan and Cheng Wang <i>Iowa State University of Science and Technology, US</i>
12:00–12:20	<i>SpikON: A Dual-Parallel and Efficient Accelerator for Online Spiking Neural Networks Learning</i> Peilin Chen and Xiaoxuan Yang <i>University of Virginia, US</i>
12:20–12:40	<i>A2CD-Gaze: Decoder-Free Compressed-Domain Eye Parsing via In-Sensor Pre-ADC Analog Convolution</i> Hong Ding, Sepehr Tabrizchi and Arman Roohi <i>University of Illinois Chicago, US</i>
11:20–12:40	Session 5B: "Emerging Technologies and Computing Paradigms"
Louis North	Chair: Ludovic Damien Blanc, EPFL & Shahin Nazarian, USC
11:20–11:40	<i>Exploiting Port-Level Parallelism and Inter-Track Skyrmion Reuse for Energy-Efficient Updates in Skyrmion Racetrack Memory</i> Cheng-Zhi Hou, Tzu-Ying Yu, Wei-Kuan Shih and Shuo-Han Chen <i>National Tsing Hua University, TW · National Yang Ming Chiao Tung University, TW</i>
11:40–12:00	<i>FUSE-GS: Frequency-Aware Pruning and Scale-Driven Rendering for Energy Efficient 3D Gaussian Splatting</i> Hanjun Choi, Hyerin Lim, Joongho Jo, Byeonghun Kwon and Jongsun Park <i>Korea University, KR</i>
12:00–12:20	<i>PALUTE: Processing-In-Memory Acceleration via Lookup Table for Edge LLM Inference</i> Runyang Tian, Yanru Chen, Weihong Xu and Tajana Rosing <i>University of California, San Diego, US · Zhejiang University, CN</i>
12:20–12:40	<i>How Many Shots Does It Take? A Noise-Aware Quantum Resource Allocation Framework</i> Prateek P. Kulkarni and Sumit K. Mandal <i>PES University, IN · Indian Institute of Science, IN</i>
12:40–13:10	Closing Remark and Award Ceremony
Louis South	Lunch (Grab-and-go)

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Srividhya Venkataraman · AMD

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Young Geun Kim · Korea University
Hamza Ouarnoughi · Canadian University Dubai
Subhankar Pal · Meta
Dong-hyeon Park · University of Michigan
Camélia Slimani · IRIT
Mengying Zhao · Shandong University

Track 2.4. Hardware and System Security

Jakub Breier (Track Co-Chair) TTControl GmbH
Kaveh Shamsi (Track Co-Chair) University of Texas at Dallas
Aydin Aysu · North Carolina State University
Anubhab Baksi · Nanyang Technological University
Debapriya Basu Roy · IIT Kanpur
Shivam Bhasin · Nanyang Technological University
Urbi Chatterjee · IIT Kanpur
Amitabh Das · AMD
Anupam Golder · Georgia Institute of Technology
Xiaolu HouSlovak · University of Technology in Bratislava
Vincent Immler · Oregon State University
Dirmanto Jap · Nanyang Technological University
Bernhard Jungk · Albstadt-Sigmaringen University
Andreas Rechberger · Intel
Marc Stottinger · RheinMain University of Applied Sciences

Track 3.1. Analog, Mixed-Signal & Emerging AI Hardware

Shaahin Angizi (Track Co-Chair) NJIT
Priyadarshini Panda (Track Co-Chair) USC
Muya Chang · NVIDIA
Gourav Datta · Case Western Reserve University
Dhruv Gajaria · Pacific Northwest National Laboratory
Pedram Khalili · Northwestern University
Pietro Mercati · Intel
M. Hassan Najafi · Case Western Reserve University
Sudeep Pasricha · Colorado State University
Muhammad Rashedul Haq Rashed · U Texas at Arlington
Dayane Reis · University of South Florida
Arman Roohi · University of Illinois at Chicago
Hanchen Ye · ElastixAI
Wenfeng Zhao · Binghamton University

Track 3.2. Digital AI Hardware & Systems

Youngjoo Lee (Track Co-Chair) KAIST
Sai Qian Zhang (Track Co-Chair) New York University
Hiromitsu Awano · Nagoya University
Youngjin Cho · Furiosa
Shaoyi Huang · Stevens Institute of Technology
Shanshi Huang · HKUST
Shubham Jain · IBM
Jan Moritz Joseph · RWTH Aachen University
Ji-Hoon Kim · Hanyang University
Bokyung Kim · North Carolina State University
Ziyun Li · Meta
Muhammad Shafique · New York University
Zishen Wan · Georgia Institute of Technology

EXTERNAL REVIEWERS

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Atharv Arun Desai
Hong Ding
Paul van Geest
Kamyar Givaki
Jimin Go
Naman Jain
Lincolnspartacus James
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Beatrice Alessandra Motetti
Deniz Najafi
Manish Paradkar
Karan Pathak
Jesper van de Pavert
Giovanni Pollo
Rohit Prasad
Rachmad Vidya Putra
Farzad Razi
Matteo Risso
Claudio Di Salvo
Ali Shafiee Sarvestani
Yonglak Son
Giuseppe Sorrentino
Yuxiang Sun
Guanchen Tao
Nuo Xu
Ivan Zaino

PROGRAM OVERVIEW

LS Louis South LN Louis North

Wednesday, Aug. 5	08:30–09:30	Registration + Breakfast	
	09:30–10:00	Welcome and Opening Remarks	
	10:00–11:00	Keynote 1 LS Naveen Verma, Princeton University and EnCharge AI "In-Memory Computing for Next-Generation AI: Low-Power Design Applied to a New Era of Circuits and Architectures"	
	11:00–11:20	Coffee Break	
	11:20–12:40	Session 1A LS <i>"ML-Driven Analysis and Optimization for Physical Design Signoff"</i>	Session 1B LN <i>"Energy-Efficient Accelerators for Edge Intelligence"</i>
	12:40–14:00	Lunch	
	14:00–15:00	Special Session LS <i>"Advancing Neuromorphic Testbeds: Integrating Memristor, Magnetic, and Superconducting Technologies for Next Generation Artificial Intelligence Computing Platforms"</i>	
	15:00–16:00	Women in Technology (WIT) – Panel Discussion LS Kathy Hoover, AMD "Powering the Future: A Conversation with Women Leaders in Electronics and Computing"	
	16:00–16:30	Women in Technology (WIT) – Student Showcase Lightning Talks LS	
	16:30–17:00	Coffee Break + WIT Poster Session LS	
	17:00–18:20	Session 2A LS <i>"Data-Efficient and Precision-Optimized AI Accelerators"</i>	Session 2B LN <i>"Robust and Efficient In-Memory Computing"</i>
Thursday, Aug. 6	08:30–09:00	Registration + Breakfast	
	09:00–10:00	Keynote 2 LS Wilfred G. van der Wiel, University of Twente and University of Münster "Reconfigurable Nonlinear Computing in Silicon"	
	10:00–10:30	Coffee Break	
	10:30–11:50	Session 3A LS <i>"Emerging Circuits for Computing and Energy"</i>	Session 3B LN <i>"Efficient Computation for Edge and Brain-Inspired AI"</i>
	11:50–12:50	Lunch	
	12:50–15:10	Posters LS	Design Contest Demos LS
	15:10–16:30	Session 4A LS <i>"Specialized Accelerators for AI Workloads"</i>	Session 4B LN <i>"Secure and Efficient Computing Architectures"</i>
	17:30–20:30	Banquet	
Friday, Aug. 7	08:30–09:00	Registration + Breakfast	
	09:00–10:00	Keynote 3 LS Hoshik Kim, SK Hynix "Near-Data Computing: The Energy Imperative"	
	10:00–11:00	Panel Discussion LS Srividhya Venkataraman, AMD "Accelerating Silicon Innovation with GenAI"	
	11:00–11:20	Coffee Break	
	11:20–12:40	Session 5A LS <i>"Efficient Sensing and Neuromorphic Accelerators"</i>	Session 5B LN <i>"Emerging Technologies and Computing Paradigms"</i>
	12:40–13:10	Closing Remark and Award Ceremony LS	
	13:10	Lunch (Grab-and-go)	